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PTO/SB/088 (10-96)
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Application Number	10/605,628
Filing Date	10/15/2003
First Named Inventor	Jiang Liu
Group Art Unit	2858 2173
Examiner Name	Unassigned JACOB, MARY
Attorney Docket Number	1372.55.PRC

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
HCJ	1	VAN MOER ET AL.; Proving the Usefulness of a 3-Port Nonlinear Vectorial Network Analyser Through Mixer Measurements; IEEE MTT-S Digest; 2003.	
HCJ	2	TERROVITIS; Noise and Distortion Analysis of Active CMOS Mixers; Qualifying Examination Proposal. MAY, 1999	
HCJ	3	FARIA ET AL.; The Use of Intermodulation Tables for Mixer Simulations; Microwave Journal; April 2002.	
HCJ	4	LIU ET AL.; A Table-Based Asymmetric System Mixer Model; Department of Electrical Engineering; University of South Florida. 2003	
HCJ	5	LIU ET AL.; Automated Measurement of Intermodulation Tables for Mixers; Center for Wireless and Microwave Information Systems; University of South Florida. 2007	

Examiner Signature	Mary C. Jacobs	Date Considered	6/9/07
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